

### **Amendments to the Specification:**

Pursuant to 37 C.F.R. § 1.121(b) kindly amend the specification as follows. Amendments to the specification are made by presenting replacement paragraphs or sections marked up to show changes made relative to the immediate prior version. The changes in any amended paragraph or section are being shown by strikethrough (for deleted matter) or underlined (for added matter).

Please add the following three paragraphs after the paragraph ending on page 7, line 19:

In one embodiment, an image on the display is addressed using an erase address waveform, which stores a charge on each subpixel to turn each subpixel ON, and selectively removes-said charge from at least one subpixel by applying an erase pulse to its corresponding electrodes, thereby turning said at least one subpixel OFF.

In another embodiment, an image on the display is addressed using a ramped voltage address waveform, which turns each subpixel ON by applying at least one voltage ramp to at least one pair of sustain electrodes to create a standardized charge at each subpixel, and selectively removes the charge from at least one subpixel by applying an erase pulse to its corresponding electrodes, thereby turning the subpixel OFF.

In a third embodiment, an image on the display is addressed using a write address waveform, which removes a charge from each subpixel, thereby turning each subpixel OFF, and adds charge to at least one subpixel by applying a voltage to its corresponding electrodes, thereby turning the subpixel ON.